

FIG. 2

Sector Address Vector I	DQ Vector I			
	DUT0	DUT1	DUT2	DUT3
Sector Addr 0 (Blocks 0-3)	0 1 0 1	0 1 0 0	1 1 0 1	0 1 1 0
Sector Addr 1 (Blocks 4-7)	0 0 1 1	1 0 1 0	0 0 1 1	1 1 1 0
Sector Addr 2 (Blocks 8-11)	0 0 0 1	0 0 0 0	1 0 0 1	1 0 0 0
Sector Addr 3 (Blocks 12-15)	1 1 0 1	0 0 1 0	0 1 0 1	0 1 1 1

FIG. 3.

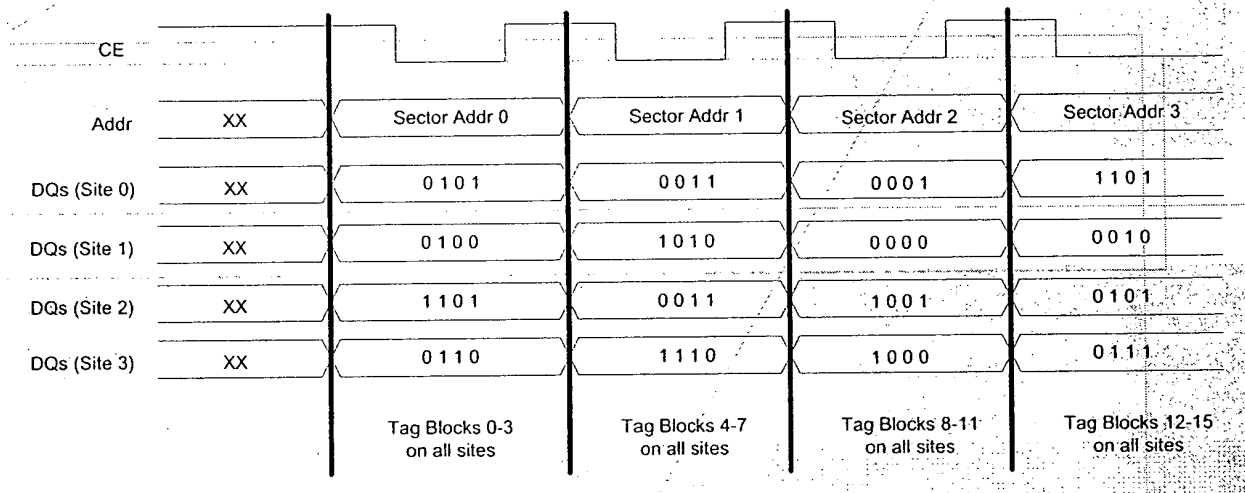


FIG. 4

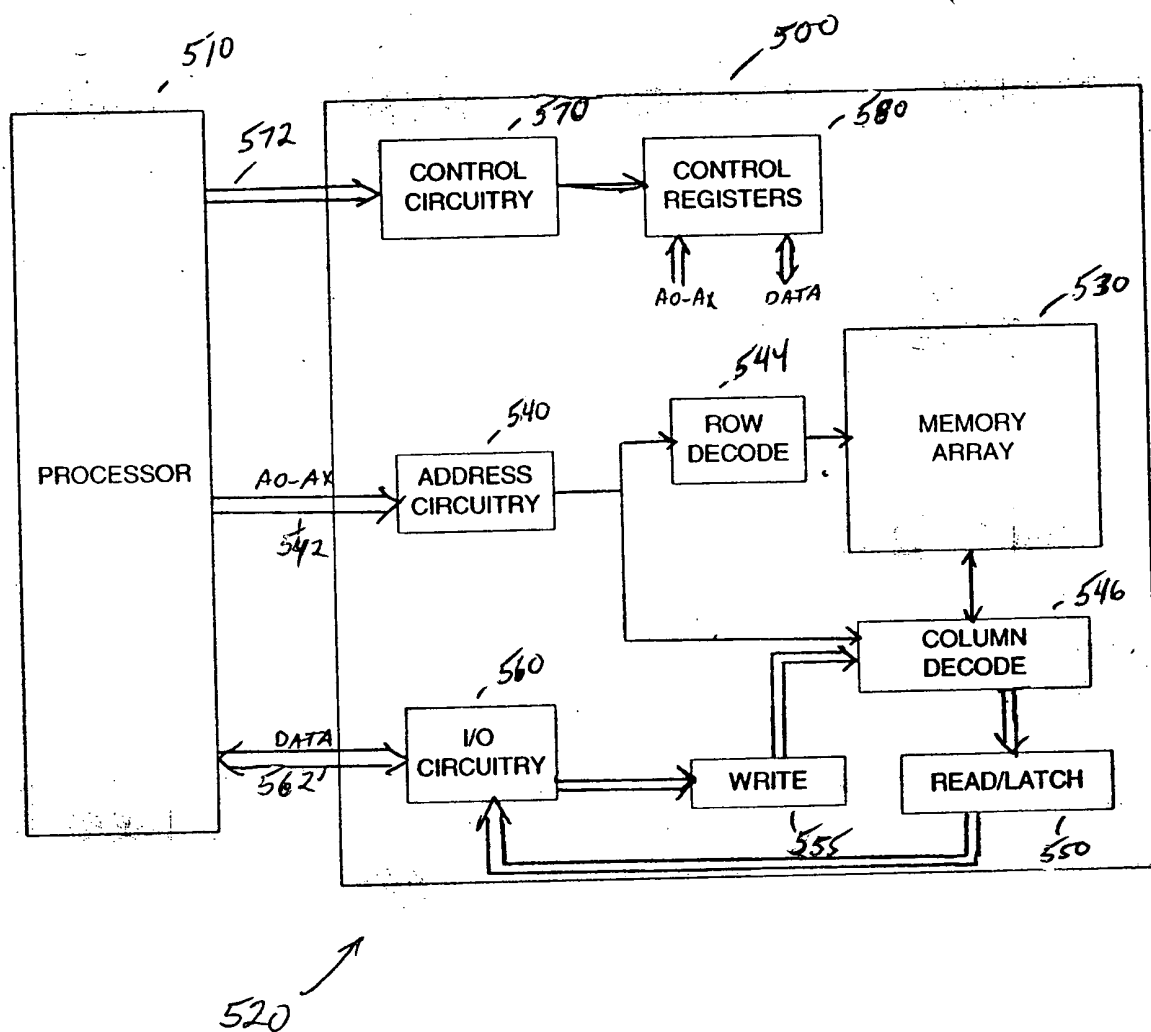


FIG. 5